

L Number	Hits	Search Text	DB	Time stamp
-	0	on\$processor near2 register near2 space	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/11/06 16:56
-	0	register near2 space same on-processor	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/11/06 16:15
-	0	register near2 space and on-processor	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/11/06 16:15
-	42	on-processor	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/11/06 16:16
-	1	on-processor same register	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/11/06 16:19
-	0	09/608,313	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/11/06 16:20
-	104	allocation same instruction same routine	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/11/06 16:20
-	5	on\$chip near2 register near2 space	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/11/06 16:56
-	56	register adj allocation and 717/140-144.ccls.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/11/06 20:23
-	199	register adj allocation and 717/150-161.ccls.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/11/06 20:23